

### SR220-SR260

#### 2.0A Schottky Barrier Rectifiers

20.4

**DO-41** 

.107 (2.7) .080 (2.0)

DIA.

#### **Features**

- ♦ Schottky Barrier Chip
- ♦ Guard Ring Die Construction for Transient Protection
- High Current Capability
- ♦ Low Power Loss, High Efficiency
- High Surge Current Capability
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications

### **Mechanical Data**

♦ Case: Molded Plastic

→ Polarity: Cathode Band

♦ Weight: 0.30 grams (approx.)

Mounting Position: AnyMarking: Type Number

Dimensions in inches and (millimeters)

# **Maximum Ratings and Electrical Characteristics**

Rating at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%

Type Number		Symbol	SR220	SR230	SR240	SR250	SR260	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		VRRM VRWM VR	20	30	40	50	60	V
RMS Reverse Voltage		VR(RMS)	14	21	28	35	42	V
Average Rectified Output Current (Note 1) @T <sub>L</sub> = 100°C		lo	2.0					Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)		IFSM	25					А
Forward Voltage	@I <sub>F</sub> = 2.0A	VFM	0.50			0.70		V
Peak Reverse Current At Rated DC Blocking Voltage	@T <sub>A</sub> = 25°C @T <sub>A</sub> = 100°C	IRM	0.5 10			mA		
Typical Junction Capacitance (Note 2)		Cj	170		140		pF	
Typical Thermal Resistance Junction to Lead		$R_{ heta}JL$	15					K/W
Typical Thermal Resistance Junction to Ambient (Note 1)		$R_{\theta}$ JA	50					K/W
Operating and Storage Temperature Range		Tj, Tstg	-65 to +150					°C

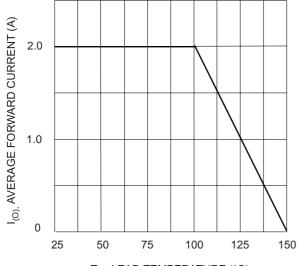
Note: 1. Valid provided that leads are kept at ambient temperature at a distance of 9.5mm from the case.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

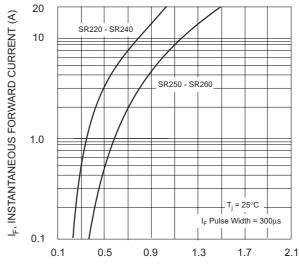


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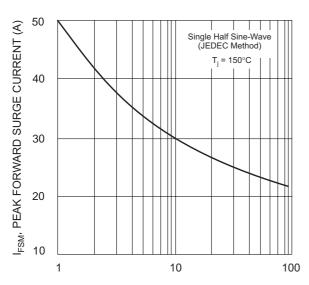
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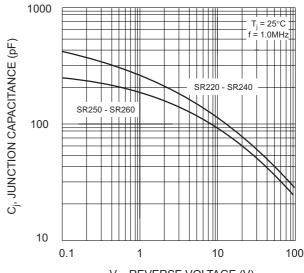
T<sub>L</sub>, LEAD TEMPERATURE (°C) Fig. 1 Forward Current Derating Curve



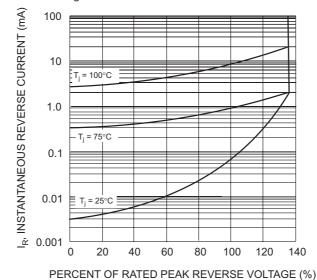
 $V_{\rm F}$ , INSTANTANEOUS FORWARD VOLTAGE (V) Fig. 2 Typical Forward Characteristics



NUMBER OF CYCLES AT 60 Hz Fig. 3 Max Non-Repetitive Peak Fwd Surge Current



V<sub>R</sub>, REVERSE VOLTAGE (V) Fig. 4 Typical Junction Capacitance



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Fig. 5 Typical Reverse Characteristics